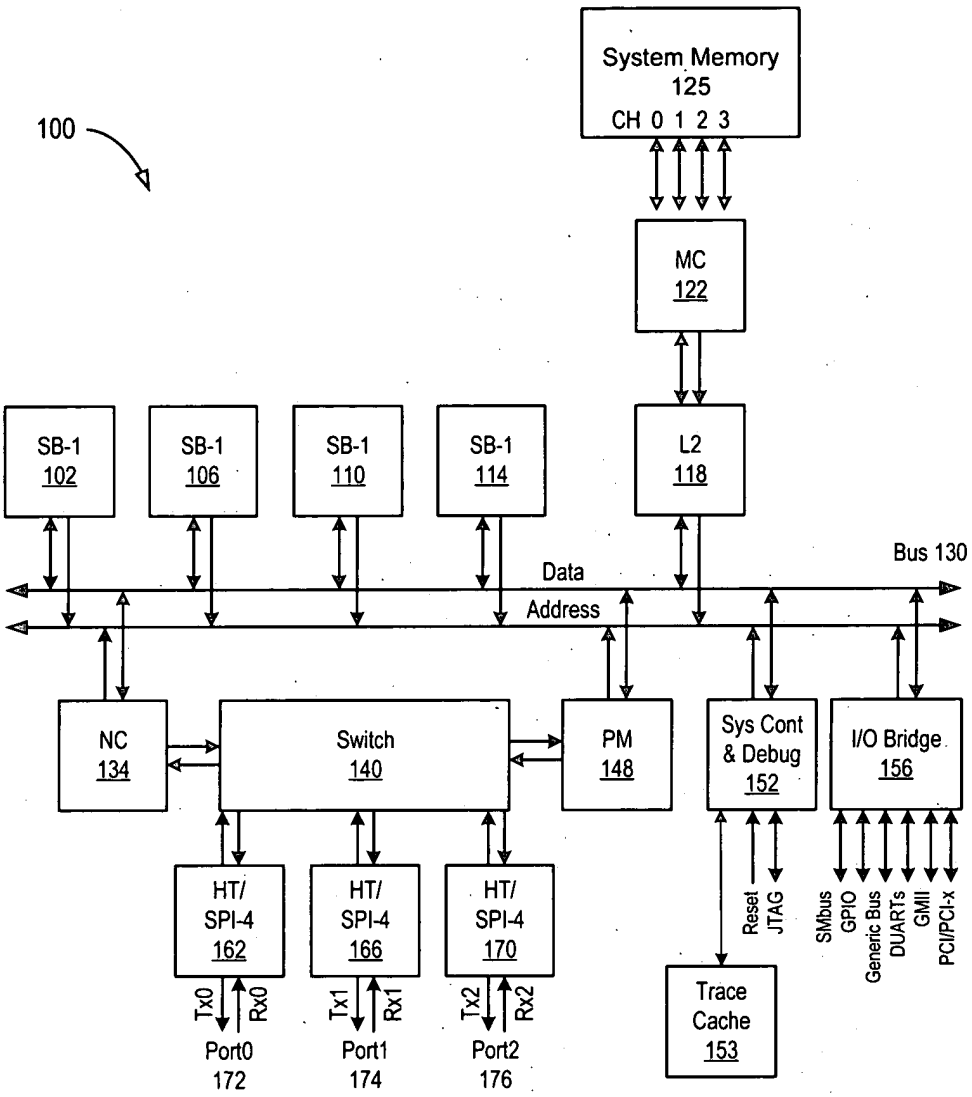
100
System Memory
125
CH 0 1 2 3
MC
122
SB-1
102
SB-1
106
SB-1
110
SB-1
114
L2
118
Bus 130
Data
Address
NC
134
Switch
140
PM
148
Sys Cont
& Debug
152
I/O Bridge
156
HT/
SPI-4
162
HT/
SPI-4
166
HT/
SPI-4
170
Tx0
Rx0
Tx1
Rx1
Tx2
Rx2
Port0
172
Port1
174
Port2
176
Trace
Cache
153
Reset
JTAG
SMbus
GPIO
Generic Bus
DUARTs
GMII
PCI/PCI-x

Figure 1

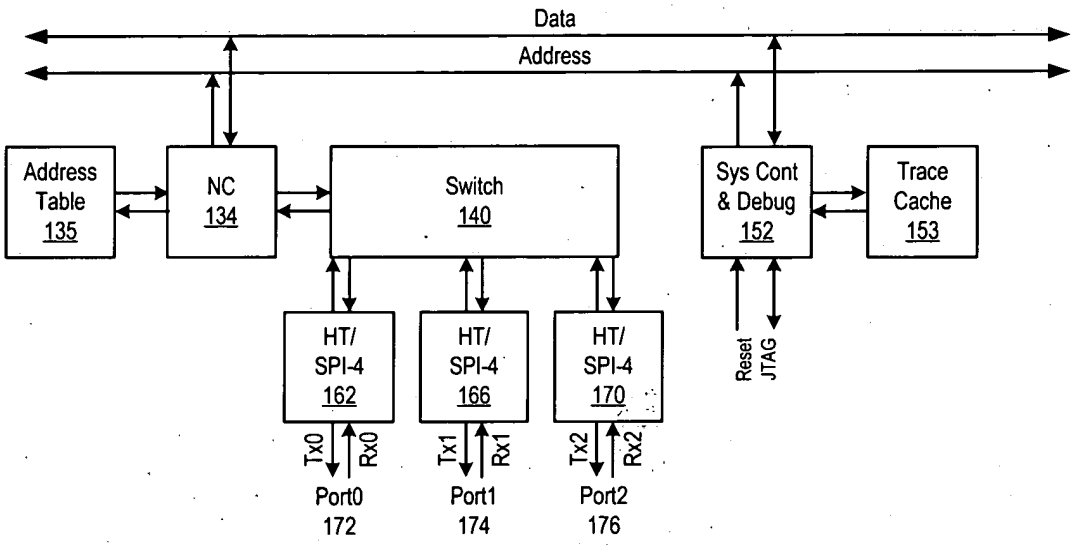
Data
Address
Address Table 135
NC 134
Switch 140
Sys Cont & Debug 152
Trace Cache 153
HT/ SPI-4 162
HT/ SPI-4 166
HT/ SPI-4 170
Reset
JTAG
Tx0
Rx0
Tx1
Rx1
Tx2
Rx2
Port0 172
Port1 174
Port2 176

Figure 2